

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	0	382/151,181,213,218,307,312	US- PGPUB; USPAT	2007/08/29 14:21	
2	BRS	L2	1105	348/362	US- PGPUB; USPAT	2007/08/29 14:23	
3	BRS	L3	3871	((picture nearl element\$1) or pel\$1 or pixel\$1) same (several or plural or array) same (zone\$1 or area\$1 or region\$1 or section\$1) same (open\$4 or aperture\$1)	US- PGPUB; USPAT	2007/08/29 14:36	
4	BRS	L4	846	3 same (activ\$4 or allow\$4 or nonallow\$4 or non- allow\$4 or nonactiv\$4 or non-activ\$4)	US- PGPUB; USPAT	2007/08/29 14:26	
5	BRS	L5	94	4 same expos\$4	US- PGPUB; USPAT	2007/08/29 14:27	
6	BRS	L6	61	5 same (lumin\$6 or light\$3 or bright\$5)	US- PGPUB; USPAT	2007/08/29 14:36	
7	BRS	L7	7	6 same intens\$5	US- PGPUB; USPAT	2007/08/29 14:32	
8	BRS	L8	1	7 same mask\$3	US- PGPUB; USPAT	2007/08/29 14:32	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
9	BRS	L9	3267	mask\$1 near10 aperture\$1 near10 expos\$4	US- PGPUB; USPAT	2007/08/29 14:33	
10	BRS	L10	33	9 same ((pixel\$1 or pel\$1) near5 (array or plural\$5 or several))	US- PGPUB; USPAT	2007/08/29 14:35	
11	BRS	L11	33	10 same mask\$1	US- PGPUB; USPAT	2007/08/29 14:35	
12	BRS	L12	6	11 same (lumin\$6 or light\$3 or bright\$5)	US- PGPUB; USPAT	2007/08/29 14:36	
13	BRS	L13	2	12 same (zone\$1 or area\$1 or region\$1 or section\$1)	US- PGPUB; USPAT	2007/08/29 14:36	
14	BRS	L14	0	1 and 11	US- PGPUB; USPAT	2007/08/29 14:37	
15	BRS	L15	1	2 and 11	US- PGPUB; USPAT	2007/08/29 14:37	
16	BRS	L16	380	4 same imag\$3	US- PGPUB; USPAT	2007/08/29 14:37	
17	BRS	L17	74	16 same mask\$3	US- PGPUB; USPAT	2007/08/29 14:37	
18	BRS	L18	19	17 same expos\$5	US- PGPUB; USPAT	2007/08/29 14:38	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
19	BRS	L19	7	18 same intensit\$3	US- PGPUB; USPAT	2007/08/29 14:39	

Day : Wednesday

Date: 8/29/2007

Time: 15:29:12

PALM INTRANET**Inventor Name Search Result**

Your Search was:

Last Name = ALTICE

First Name = PETER

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>10653971</u>	Not Issued	80	09/04/2003	METAL MASK FOR LIGHT INTENSITY DETERMINATION AND ADC CALIBRATION	ALTICE, PETER P.
<u>10663709</u>	7215361	150	09/17/2003	METHOD FOR AUTOMATED TESTING OF THE MODULATION TRANSFER FUNCTION IN IMAGE SENSORS	ALTICE, PETER P.
<u>10721169</u>	Not Issued	77	11/26/2003	Anti-blooming storage pixel	ALTICE, PETER P.
<u>10721190</u>	Not Issued	30	11/26/2003	Image sensor with a capacitive storage node linked to transfer gate	ALTICE, PETER P.
<u>10721191</u>	Not Issued	30	11/26/2003	Image sensor with a gated storage node linked to transfer gate	ALTICE, PETER P.
<u>10766012</u>	7196304	150	01/29/2004	ROW DRIVER FOR SELECTIVELY SUPPLYING OPERATING POWER TO IMAGER PIXEL	ALTICE, PETER P.
<u>11265154</u>	7176434	150	11/03/2005	ROW DRIVEN IMAGER PIXEL	ALTICE, PETER P.
<u>11472435</u>	Not Issued	30	06/22/2006	Method for automated testing of the modulation transfer function in image sensors	ALTICE, PETER P.
<u>11526005</u>	Not Issued	25	09/25/2006	Anti-blooming storage pixel	ALTICE, PETER P.
<u>11643681</u>	Not Issued	41	12/22/2006	Row driven imager pixel	ALTICE, PETER P.
<u>11882690</u>	Not Issued	17	08/03/2007	Row driven imager pixel	ALTICE, PETER P.
<u>10751440</u>	Not Issued	41	01/06/2004	Imager device with dual storage nodes	ALTICE, PETER PARKER

<u>11055105</u>	Not Issued	71	02/11/2005	Method and apparatus for dark current and hot pixel reduction in active pixel image sensors	ALTICE, PETER PARKER
<u>11488110</u>	Not Issued	30	07/18/2006	Method, apparatus and system for charge injection suppression in active pixel sensors	ALTICE, PETER PARKER
<u>11737186</u>	Not Issued	30	04/19/2007	METHOD AND APPARATUS FOR DARK CURRENT AND HOT PIXEL REDUCTION IN ACTIVE PIXEL IMAGE SENSORS	ALTICE, PETER PARKER

Inventor Search Completed: No Records to Display.

Search Another: Inventor

Last Name

altice

First Name

peter

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

Day : Wednesday

Date: 8/29/2007

Time: 15:29:47

PALM INTRANET**Inventor Name Search Result**

Your Search was:

Last Name = MCKEE

First Name = JEFFREY

Application#	Patent#	Status	Date Filed	Title	Inventor Name
08401740	5726085	150	03/09/1995	METHOD OF FABRICATING A DYNAMIC RANDOM ACCESS MEMORY (DRAM) CELL CAPACITOR USING HEMISPHERICAL GRAIN (HSG) POLYSILICON AND SELECTIVE POLYSILICON ETCHBACK	MCKEE, JEFFREY
60007140	Not Issued	159	11/01/1995	METHOD FOR FORMING AN ELECTRONIC DEVICE CONTACT POST AND STRUCTURE RESULTING THEREFROM	MCKEE, JEFFREY
09219240	6194306	150	12/22/1998	MASK AND METHOD FOR FORMING DYNAMIC RANDOM ACCESS MEMORY (DRAM) CONTACTS	MCKEE, JEFFREY A
09754728	6417091	150	01/04/2001	Mask and method for forming dynamic random access memory (DRAM) contacts	MCKEE, JEFFREY A.
09776212	6528888	150	02/02/2001	MEMORY INTEGRATED CIRCUIT AND METHOD	MCKEE, JEFFREY A.
09816962	7176109	150	03/23/2001	METHOD FOR FORMING RAISED STRUCTURES BY CONTROLLED SELECTIVE EPITAXIAL GROWTH OF FACET USING SPACER	MCKEE, JEFFREY A.
10001441	6569733	150	10/30/2001	GATE DEVICE WITH RAISED CHANNEL AND METHOD	MCKEE, JEFFREY A.
10046497	Not Issued	121	10/26/2001	Method for forming raised structures by controlled selective epitaxial growth of facet using spacer	MCKEE, JEFFREY A.
10053300	7105899	150	01/17/2002	TRANSISTOR STRUCTURE	MCKEE, JEFFREY

				HAVING REDUCED TRANSISTOR LEAKAGE ATTRIBUTES	A.
<u>10133788</u>	6569734	150	04/26/2002	METHOD FOR TWO-SIDED FABRICATION OF A MEMORY ARRAY	MCKEE, JEFFREY A.
<u>10163476</u>	6522577	150	06/05/2002	SYSTEM AND METHOD FOR ENABLING CHIP LEVEL ERASING AND WRITING FOR MAGNETIC RANDOM ACCESS MEMORY DEVICES	MCKEE, JEFFREY A.
<u>10341768</u>	6650564	150	01/13/2003	SYSTEM AND METHOD FOR ENABLING CHIP LEVEL ERASING AND WRITING FOR MAGNETIC RANDOM ACCESS MEMORY DEVICES	MCKEE, JEFFREY A.
<u>10379494</u>	Not Issued	121	03/04/2003	Method for forming raised structures by controlled selective epitaxial growth of facet using spacer	MCKEE, JEFFREY A.
<u>10456585</u>	Not Issued	71	06/09/2003	Imager with tuned color filter	MCKEE, JEFFREY A.
<u>10653971</u>	Not Issued	80	09/04/2003	METAL MASK FOR LIGHT INTENSITY DETERMINATION AND ADC CALIBRATION	MCKEE, JEFFREY A.
<u>10721169</u>	Not Issued	77	11/26/2003	Anti-blooming storage pixel	MCKEE, JEFFREY A.
<u>10721190</u>	Not Issued	30	11/26/2003	Image sensor with a capacitive storage node linked to transfer gate	MCKEE, JEFFREY A.
<u>10721191</u>	Not Issued	30	11/26/2003	Image sensor with a gated storage node linked to transfer gate	MCKEE, JEFFREY A.
<u>10931513</u>	7157324	150	09/01/2004	TRANSISTOR STRUCTURE HAVING REDUCED TRANSISTOR LEAKAGE ATTRIBUTES	MCKEE, JEFFREY A.
<u>11106466</u>	Not Issued	71	04/15/2005	Dual conversion gain pixel using Schottky and ohmic contacts to the floating diffusion region and methods of fabrication and operation	MCKEE, JEFFREY A.
<u>11125246</u>	Not Issued	71	05/10/2005	Pixel with gate contacts over active region and method of forming same	MCKEE, JEFFREY A.
<u>11126275</u>	Not Issued	30	05/11/2005	High fill factor multi-way shared pixel	MCKEE, JEFFREY A.

<u>11126307</u>	Not Issued	41	05/11/2005	Split trunk pixel layout	MCKEE, JEFFREY A.
<u>11178324</u>	Not Issued	30	07/12/2005	Dual conversion gain gate and capacitor and HDR combination	MCKEE, JEFFREY A.
<u>11178798</u>	Not Issued	30	07/12/2005	Pixel with transfer gate with no isolation edge	MCKEE, JEFFREY A.
<u>11178803</u>	Not Issued	71	07/12/2005	Method and apparatus providing capacitor on an electrode of an imager photosensor	MCKEE, JEFFREY A.
<u>11193322</u>	Not Issued	71	08/01/2005	Dual conversion gain gate and capacitor combination	MCKEE, JEFFREY A.
<u>11200052</u>	Not Issued	30	08/10/2005	Image pixel reset through dual conversion gain gate	MCKEE, JEFFREY A.
<u>11201327</u>	Not Issued	41	08/11/2005	Pixel cells in a honeycomb arrangement	MCKEE, JEFFREY A.
<u>11201332</u>	Not Issued	30	08/11/2005	High dynamic range imaging device using multiple pixel cells	MCKEE, JEFFREY A.
<u>11207744</u>	Not Issued	30	08/22/2005	HDR/AB on multi-way shared pixels	MCKEE, JEFFREY A.
<u>11209779</u>	Not Issued	30	08/24/2005	Capacitor over red pixel	MCKEE, JEFFREY A.
<u>11211651</u>	Not Issued	41	08/26/2005	Implanted isolation region for imager pixels	MCKEE, JEFFREY A.
<u>11213936</u>	Not Issued	30	08/30/2005	Method and apparatus providing a two-way shared storage gate on a four-way shared pixel	MCKEE, JEFFREY A.
<u>11213937</u>	7244918	150	08/30/2005	METHOD AND APPARATUS PROVIDING A TWO-WAY SHARED STORAGE GATE ON A FOUR-WAY SHARED PIXEL	MCKEE, JEFFREY A.
<u>11512478</u>	Not Issued	30	08/30/2006	Method for forming raised structures by controlled selective epitaxial growth of facet using spacer	MCKEE, JEFFREY A.
<u>11512574</u>	Not Issued	30	08/30/2006	Method for forming raised structures by controlled selective epitaxial growth of facet using spacer	MCKEE, JEFFREY A.
<u>11526005</u>	Not Issued	25	09/25/2006	Anti-blooming storage pixel	MCKEE, JEFFREY A.
<u>11713754</u>	Not Issued	25	03/05/2007	Pixel cells in a honeycomb arrangement	MCKEE, JEFFREY A.
<u>60884565</u>	Not	20	01/11/2007	MISSING PIXEL	MCKEE, JEFFREY

	Issued			ARCHITECTURE	A.
<u>07252227</u>	4922731	150	09/30/1988	QUARTZ CONDUCTIVE BAFFLES FOR HEAT REMOVAL AND METHOD	MCKEE, JEFFREY A.
<u>07291719</u>	5084126	150	12/29/1988	METHOD AND APPARATUS FOR UNIFORM FLOW DISTRIBUTION IN PLASMA REACTORS	MCKEE, JEFFREY A.
<u>08224947</u>	5596207	150	04/08/1994	APPARATUS AND METHOD FOR DETECTING DEFECTS IN INSULATIVE LAYERS OF MOS ACTIVE DEVICES	MCKEE, JEFFREY A.
<u>08475246</u>	Not Issued	161	06/07/1995	APPARATUS AND METHOD FOR DETECTING DEFECTS IN INSULATIVE LAYERS OF MOS ACTIVE DEVICES	MCKEE, JEFFREY A.
<u>08679041</u>	5804088	150	07/12/1996	INTERMEDIATE LAYER LITHOGRAPHY	MCKEE, JEFFREY A.
<u>08707576</u>	5739052	150	09/04/1996	APPARATUS AND METHOD FOR DETECTING DEFECTS IN INSULATIVE LAYERS OF MOS ACTIVE DEVICES	MCKEE, JEFFREY A.
<u>08736542</u>	Not Issued	163	10/24/1996	METHOD FOR FORMING AN ELECTRONIC DEVICE CONTACT POST AND STRUCTURE RESULTING THEREFROM	MCKEE, JEFFREY A.
<u>08742194</u>	Not Issued	169	10/31/1996	OXIDE HARDMASK PROFILE CONTROL FOR GATE ETCH	MCKEE, JEFFREY A.
<u>08856855</u>	5960304	150	05/15/1997	METHOD FOR FORMING A CONTACT TO A SUBSTRATE	MCKEE, JEFFREY A.
<u>08970222</u>	Not Issued	164	11/14/1997	MEMORY INTEGRATED CIRCUIT AND METHOD	MCKEE, JEFFREY A.

[Search and Display More Records.](#)

Search Another: Inventor	Last Name	First Name
	mckee	jeffrey
		<input type="button" value="Search"/>

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

Day : Wednesday

Date: 8/29/2007
Time: 15:32:57**PALM INTRANET****Inventor Name Search Result**

Your Search was:

Last Name = WALIGORSKI

First Name = GRZEGORZ

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10653971	Not Issued	80	09/04/2003	METAL MASK FOR LIGHT INTENSITY DETERMINATION AND ADC CALIBRATION	WALIGORSKI, GRZEGORZ M.

Inventor Search Completed: No Records to Display.

Search Another: Inventor**Last Name**

waligorski

First Name

grzegorz

Search

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | Home page